



INDIAN INSTITUTE OF TECHNOLOGY KANPUR
DEPARTMENT OF CIVIL ENGINEERING
STRUCTURAL ENGINEERING LABORATORY

DR. SAMIT RAY CHAUDHURI
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February 15, 2018

Enquiry no. CE/STR/2017-18/Feb/03

We would like to invite the sealed quotations for the supply and installation of the following items with all relevant accessories.

Item Name: Windows based Modular Data Acquisition System Programmable with Graphical Programming Environment

Major Specifications

Description	Function	Specifications
System Controller	Embedded MXIe Controller (Computing Device) To have a complete system contained within the chassis with no external PC. The bus architecture should be on PXI or PXI Express, and choice of Operating System between Windows and Real-Time.	<ul style="list-style-type: none"> • PXI/PXIe Compatible • Remote Control and System Expansion through MXI-Express • Compatible for Daisy Chaining with PXIe chassis using MXI-Express Interface • At least 3.2 GB/s system throughput • Ability to share the clock from master controller for synchronization
Chassis	To provide the power, cooling, and communication buses of PCI and PCI Express for the controller and modules.	<ul style="list-style-type: none"> • PXI/PXIe Compatible • At least 4 hybrid slots, 2 PXI Express slots, 1 PXI Express system timing slot • High performance - up to 2 GB/s per slot dedicated bandwidth and 8 GB/s system bandwidth • Forced air cooling with inbuilt fan/fans • Compatibility with PXI, PXI Express, CompactPCI, and CompactPCI Express modules
Module 1 Quantity- 1	High-accuracy and High speed Accelerometer Data Acquisition	<ul style="list-style-type: none"> • PXI / PXIe Compatible • At least 6 simultaneously sampled analog inputs at up to 1.25 MS/s with 500 kHz bandwidth • Built-in analog and digital filtering for accurate frequency domain measurements • 24-bit resolution ADCs with 115 dB dynamic range for low-noise measurements • 4 gain input ranges: ± 0.5 V, ± 1 V, ± 5 V, ± 10 V • Integrated charge measurement and voltage or IEPE excitation configurable on a per channel basis • Up to 20MS/s per channel measurements in time



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		domain mode
Module 2 Quantity-1	High-accuracy and High speed Strain Based Data Acquisition	<ul style="list-style-type: none"> • PXI / PXIe Compatible • At least 8 simultaneously sampled analog input channels; 25 kS/s/ch sampling rate • At least 24-bit ADC per channel; antialias filtering • Software-selectable excitation voltage per channel (0.625 V to 10 V) must be available • Built-in programmable quarter-, half-, and full-bridge completion and shunt calibration per channel • Multidevice triggering and synchronization via PXI Express; smart sensor (TEDS) support

Other Requirements:

- The Supplier should be the OEM and should produce the OEM certificate.
- The system should be an already existing and well proven product.

Kindly send your offer (Original, Signed with the name of signing authority) in a sealed envelope with label quotation, for the above items mentioning the following:

1. Cost of the item with technical specifications in detail
2. Warranty period
3. Delivery time
4. Educational discount applicable considering end use for research and teaching
5. Payment terms
6. Proprietary Certificate, if applicable
7. Any other relevant details

An early reply latest by 05th March 2018 will be highly appreciated. For any further information/clarifications in this regard, please write back to us at samitrc@iitk.ac.in or mohitd@iitk.ac.in.

Thanking you...

Samit Ray Chaudhuri

-: Last date has been extended till 19 March 2018